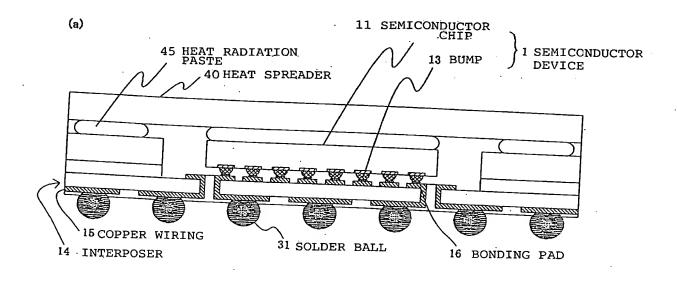
Fig. 1 Prior Art



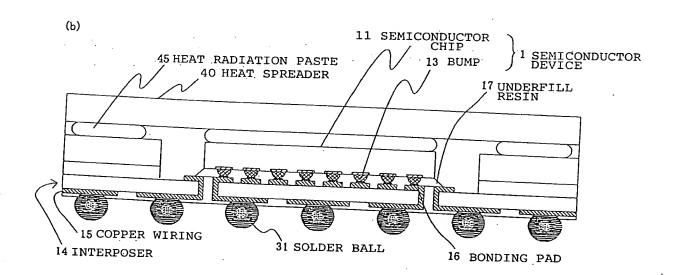


Fig. 2 Prior Art

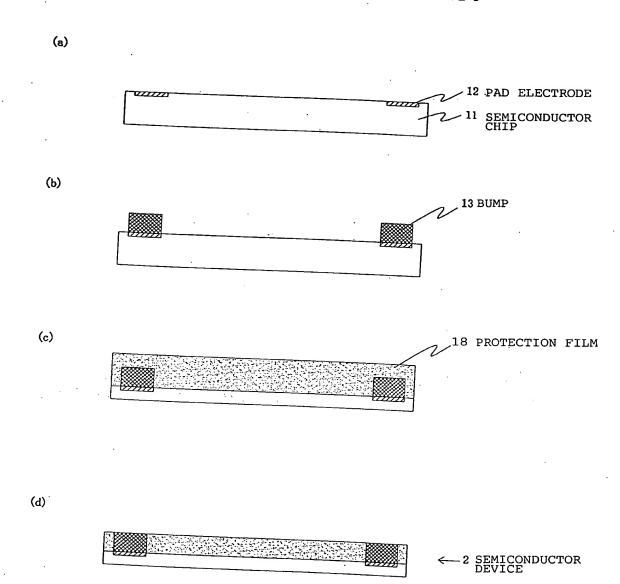


Fig. 3 Prior Art

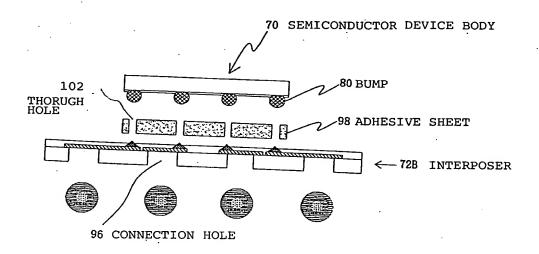


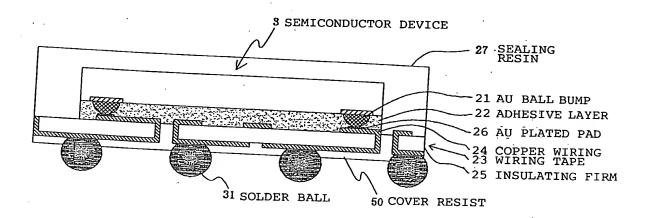
Fig. 4

(a) 12 PAD ELECTRODE 11 SEMICONDUCTOR CHIP (b) 21 AU BALL BUMP 12 PAD ELECTRODE -11 SEMICONDUCTOR CHIP (c) 22 ADHESIVE LAYER 21 AU BALL BUMP 12 PAD ELECTRODE -11 SEMICONDUCTOR CHIP (d) 22 ADHESIVE LAYER 21 AU BALL BUMP 12 PAD ELECTRODE - 11 SEMICONDUCTOR CHIP

3 SEMICONDUCTOR DEVICE

Fig. 5

(a)



(b)

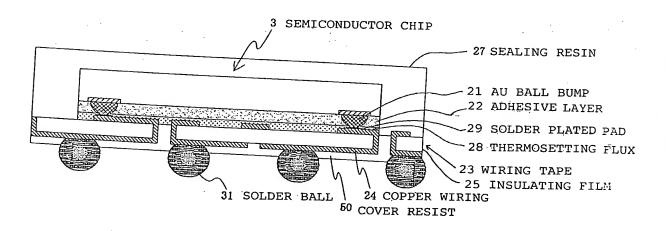


Fig. 6

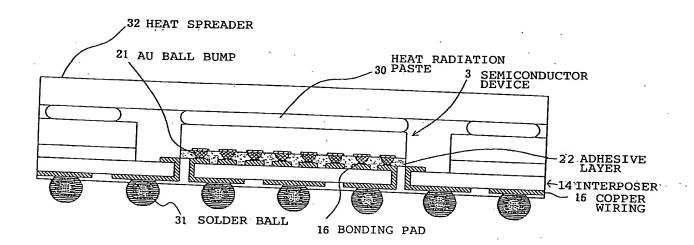


Fig. 7

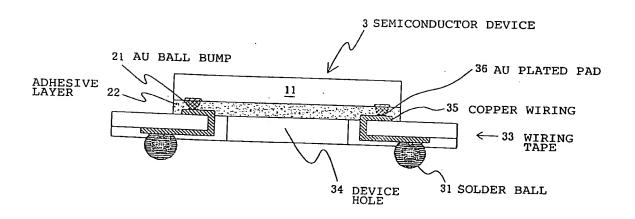
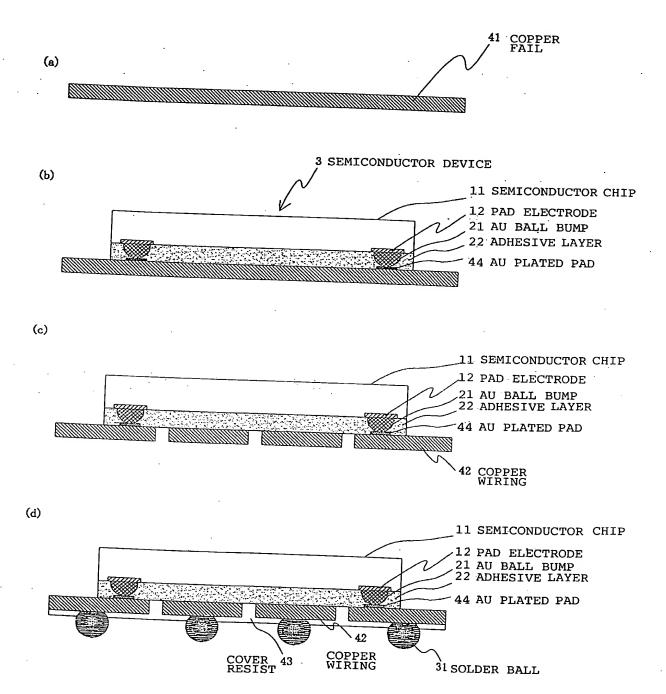


Fig. 8



(a)

Fig. 9

27 SEALING RESIN

11 SEMICONDUCTOR CHIP

12 PAD ELECTRODE

21 AU BALL BUMP

22 ADHESIVE LAYER

44 AU PLATED PAD

42 COPPER WIRING

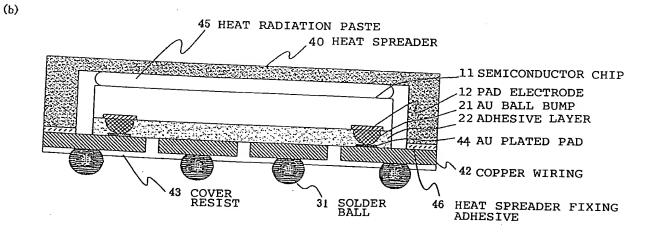


Fig. 10

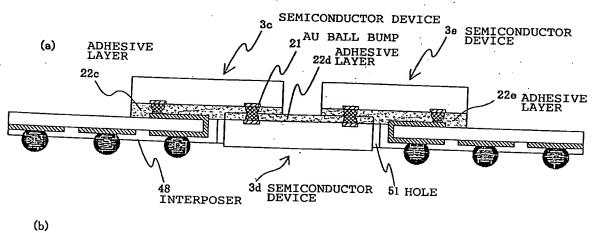
(g)

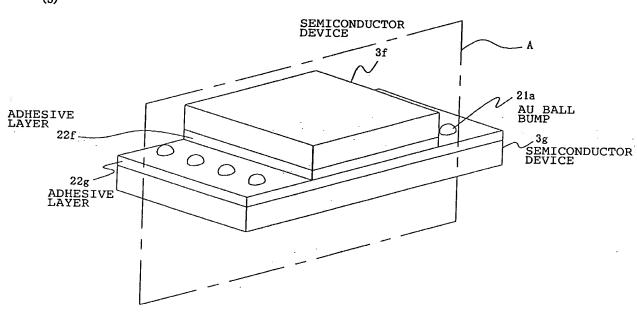
11 SEMICONDUCTOR CHIP 12 PAD ELECTRODE
21 AU BALL BUMP
22 ADHESIVE LAYER -44:AU PLATED PAD 42 COPPER WIRING 31 SOLDER BALL 43 COVER RESIST

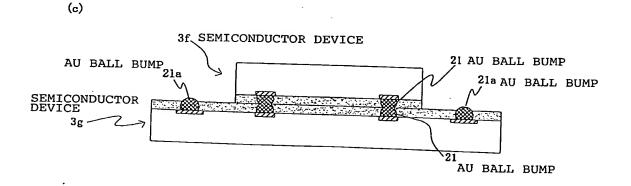
11 SEMICONDUCTOR CHIP 12 PAD ELECTRODE
21 AU BALL BUMP
22 ADHESIVE LAYER
44 AU PLATED PAD 142 COPPER WIRING 47 SEALING RESIN 9

31 SOLDER BALL

Fig. 11







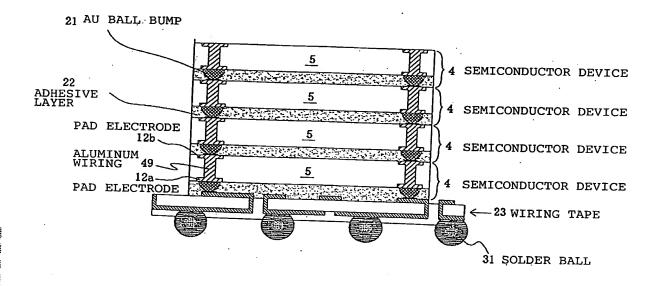


Fig. 13

